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Digital Logic Multiple Choice Questions

Multiple choice Questions Digital Logic Design. 1. How many bits must each word have in one-to-four line de-multiplexer to be implemented using a memory? A. 8 bit. B. 4 bits.

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Digital Logic Design Multiple Choice Questions with Answers pdf are useful for the students of IT preparing for competitive & academic exams. 1. Decimal means base \_\_\_\_\_. Ans. 10. 2. The value of the bit pattern 11111111 is \_\_\_\_\_. Ans. 255. 3. The range of values represented by an 8-bit binary number is \_\_\_\_\_. Ans. 0 to 255. 4.

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